

FIG.1

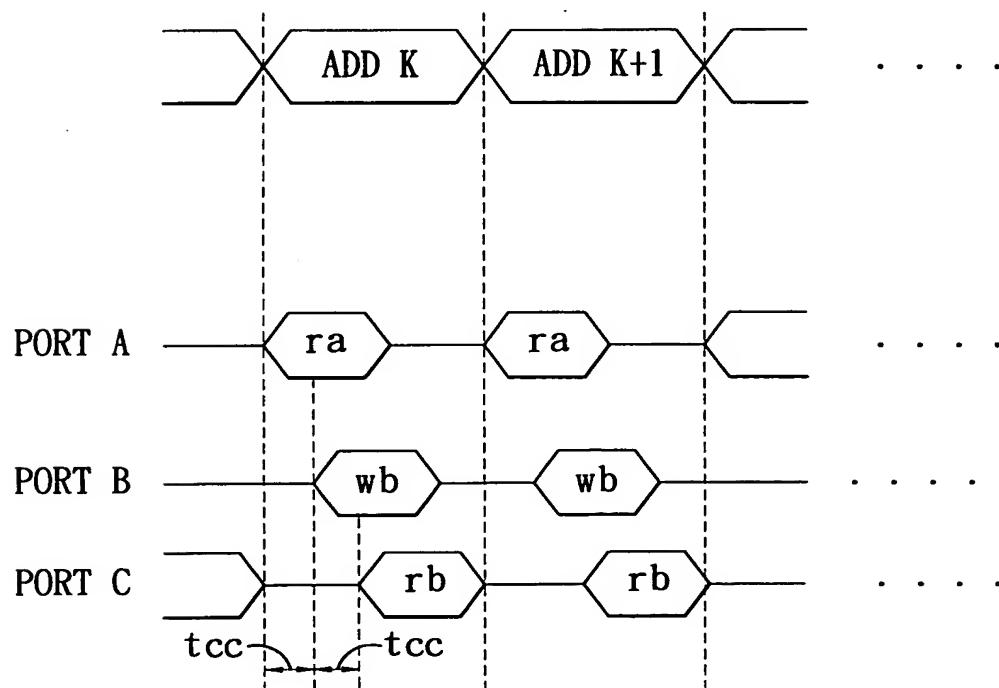


FIG.2

FIG.3

Extended March C -Test						TEST TIME
SINGLE PORT	PA: $\Downarrow (wa) \uparrow (ra, wb) \uparrow (rb, wa) \Downarrow (ra, wb, rb) \Downarrow (rb, wa, ra) \uparrow (ra)$					
TWO-PORT	PA: $\Downarrow (wa) \uparrow (ra) \uparrow (rb) \Downarrow (ra, rb) \Downarrow (rb, ra) \uparrow (ra)$	PB: $(-)$	(wb)	(wa)	(wb, -)	(wa, -)
THREE-PORT	PA: $\Downarrow (wa) \uparrow (ra) \uparrow (rb) \Downarrow (ra) \Downarrow (rb) \Downarrow (ra)$	PB: $(-)$	(wb)	(wa)	(wb)	(wa)
	PC: $(-)$	(-)	(-)	(-)	(rb)	(ra)

PORT A	$\uparrow(w0) \uparrow(r0w1) \uparrow(r1w0)$	$\left(\begin{array}{c} r0 \\ w1 \end{array} \right)$	$\uparrow(r1) \uparrow(r0)$
PORT B	$(-) \quad (r1r0) \quad (r0r1)$	$\downarrow \left(\begin{array}{c} w0 \\ r1 \end{array} \right)$	$(-)$

FIG. 4